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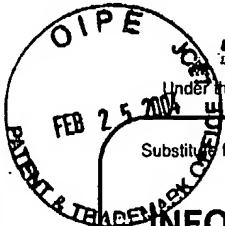
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Substitute for form 1449B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

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of

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Complete If Known

<i>Application Number</i>	10/625,276
<i>Filing Date</i>	July 23, 2003
<i>First Named Inventor</i>	Garrett
<i>Art Unit</i>	Unknown
<i>Examiner Name</i>	Unknown
<i>Attorney Docket Number</i>	RAMB-01049US1

NON PATENT LITERATURE DOCUMENTS

**Examiner
Signature**

Kevin Verhey

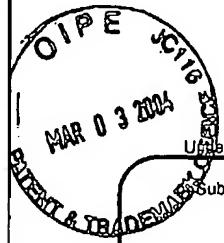
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1

Complete if Known	
Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Perego
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	RAMB-01049US1

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

**Examiner
Signature**

Kevi Verhege

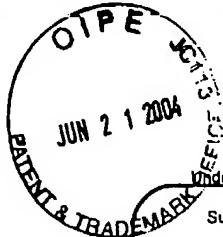
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Sheet

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<i>Complete if Known</i>	
Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Perego
Art Unit	2818
Examiner Name	Unknown
Attorney Docket Number	RAMB-01049US1

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)	MM-DD-YYYY			

Examiner Signature		Date Considered	7/22/04
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First Named Inventor	Garrett
Art Unit	2818
Examiner Name	Unknown
Attorney Docket Number	RAMB-01049US1

NON PATENT LITERATURE DOCUMENTS

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1

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Complete if Known

Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Richard E. Perego
Art Unit	
Examiner Name	

Attorney Docket Number RAMB-01049US1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number <small>Number-Kind Code² (if known)</small>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
EV		US-5,953,215	09-14-1999	Karabatsos	
EV		US-6,142,830	11-07-2000	Looeffler	
EV		US-6,185,644 B1	02-06-2001	Farmwald et al.	
EV		US-6,266,252 B1	07-24-2001	Karabatsos	
EV		US-6,317,352 B1	11-13-2001	Halbert et al.	
EV		US-6,414,904 B2	07-02-2002	So et al.	
EV		US-6,446,158 B1	09-03-2002	Karabatsos	
EV		US-6,493,250 B2	12-10-2002	Halbert et al.	
EV		US-6,530,006 B1	03-04-2003	Dodd et al.	
EV		US-6,643,752 B1	11-04-2003	Donnelly et al.	
EV		US-6,449,679 B2*	09-10-2002	Ryan	
EV		US-6,108,731*	08-22-2000	Suzuki et al.	
EV		US-5,701,313*	12-23-1997	Purdham	
EV		US-5,068,650*	11-26-1991	Fernandez et al.	
EV		US-4,947,257*	08-07-1990	Fernandez et al.	
EV		US-5,513,135*	04-30-1996	Dell et al.	
EV		US-6,065,092*	05-16-2000	Roy	
EV		US-6,185,644*	02-06-2001	Farmwald et al.	<i>duplicate</i>

FOREIGN PATENT DOCUMENTS

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Kevin Terhegge

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7/20/04

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Sheet 2

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Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Richard E. Perego
Art Unit	
Examiner Name	
Attorney Docket Number	RAMB-01049US1

U. S. PATENT DOCUMENTS

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Kevin Verheyen

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First Named Inventor	Richard E. Perego
Art Unit	
Examiner Name	
Attorney Docket Number	RAMB-01049US1

NON PATENT LITERATURE DOCUMENTS

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RV		Intel Developer Forum, "DDR2 Memory In 2004 Servers - Recipes For Successful Designs", September 2003	
RV		QBM Alliance, Platform Conference, "Quad Band Memory (QBM tm): DDR 200/266/333 devices producing DDR400/533/667, Jan. 23-24, 2002	
RV		KARABATSOS, C., "Quad Band Memory (QBM) Technology", Kentron Technologies, Inc., April 2001	
RV		Quad Band Memory (QBM), "The 'QBM Enabled' VIA PT880/PM880 Chipset Solutions with the Fastest Memory",	
RV		WEBER, F., "The AMD Athlon Processor: Future Directions", AMD	
RV		"Intel 82804AA Memory Repeater Hub for SDRAM (MRH-S) Datasheet", Intel Corp., pp. 2-48 (Nov. 1999)	
RV		"Intel 82805AA Memory Translater Hub (MTH) Datasheet", Intel Corp., pp. 1-48 (Nov. 1999)	
RV		*CATALDO, A., "TI Fields DSP-Based Media Processor on a DIMM", EE Times (Jan. 2000)	
RV		*MAC WILLIAMS, P., "PC Memory Directions For 1999 and Beyond", Intel Developer Forum, pp. 1-10 (Sep. 1998)	
RV		*JOHNSON, B., "Direct RDRAM Basic Architecture and Program Overview", Intel Developer Forum, pp. 1-14 (Sep. 1998)	

Examiner Signature

Kevin Verlage

Date Considered

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U.S. PATENT AND TRADEMARK OFFICE

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Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Richard E. Perego
Art Unit	
Examiner Name	

Sheet

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Attorney Docket Number

RAMB-01049US1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
RV		*GILLINGHAM, P., "SLDRAM Architectural and Functional Overview", SDRAM Consortium, SDRAM Inc., pp. 1-14 (Aug. 1997)	
RV		**Draft Standard for a High-Speed Memory Interface (Synclink)", Draft 0.99 IEEE P1596.7-199X, IEEE Inc., pp. 1-51 (1996)	
RV		"WIGGERS, H., "SyncLink A Proposal for an Implementation of IEEE P1596.4", Hewlett Packard, pp. 1-20 (Mar. 1995)	
RV		**KMM377S1620CT2 SDRAM Module Datasheet", Samsung Electronics Inc., pp. 1-2 (Nov. 1998)	
RV		**SLD4M18DR400 4MEG x 18 SDRAM Datasheet", SDRAM Inc., pp. 1-69 (July 1998)	
RV		*POULTON, J., "Signaling in High Performance Memory Systems", IEEE Solid State Circuits Conference, slides 1-59 (Feb. 1999)	
RV		**SLDRAM 400Mb/s/pin Command/Address Repeater", SLD10400 Rev .5, SDRAM Consortium, pp. 1-12 (Jan. 7, 1998)	
RV		*Minutes of meeting no. 75 JEDEC JC-42.3 Committee on RAM Memories, and referenced attachment "Y" of the presentation titled "SyncLink", May 24, 1995, New Orleans	

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